

Substitute for Form 1449/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)



Complete if Known

Application Number	10/038,036
Filing Date	January 2, 2002
First Named Inventor:	Edward T. Grochowski, et al.
Art Unit	2111
Examiner Name	Kim, Kenneth S.

Sheet	1	of	2	Attorney Docket Number	42P13133
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U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)				
KK		US-	2002/0087836 A1	07-04-2002	Jourdan, Stephan J. et al.	712/217
KK		US-	5,838,941	11-17-1998	Valentine, Robert C. et al.	712/217
KK		US-	5,732,234	03-1998	Vassiliadis et al.	712/200
KK		US-	5,546,597	08-1996	Martell et al.	712/23
KK		US-	5,214,763	05-1993	Blaner et al.	712/212
KK		US-	5,051,940	09-1991	Vassiliadis et al.	708/524
KK		US-	4,819,155	04-1989	Wulf et al.	712/207
KK		US-	4,594,655	06-1986	Hao et al.	712/218
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				

Examiner Signature	/Kenneth Kim/	Date Considered	06/29/2006
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Sheet	2	of	2	Application Number	10/038,036
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				Examiner Name	Kim, Kenneth S.
				Attorney Docket Number	42P13133

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
KK		JOURDAN, STEPHAN, et al., "A Novel Renaming Scheme to Exploit Value Temporal Locality through Physical Register Reuse and Unification", <u>IEEE 1998</u> , (1998), 10 pgs.	
KK		MUCHNICK, STEVEN S., "Forward Substitution", <u>Advanced Compiler Design and Implementation</u> , Copyright 1997 by Morgan Kaufmann Publisher, Section 13.1.3 pg. 396, (1997) 3 pgs.	
KK		PHILLIPS, et al., "High Performance 3-1 Interlock Collapsing ALU's", <u>IEEE</u> , (1994) 12 pgs.	
KK		JACOBSON, et al., "Instruction Pre-Processing in Trace Processors", (1999) 5 pgs.	

Examiner Signature	/Kenneth Kim/	Date Considered	06/29/2006
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